TRANSMITTAL

Electronic Version v1.1

Stylesheet Version v1.1.0

Title of
Invention

METHOD OF INTEGRATING HIGH VOLTAGE METAL OXIDE SEMICONDUCTOR DEVICES AND SUBMICRON METAL OXIDE SEMICONDUCTOR DEVICES

Application Number:

Date:

First Named Applicant:

Ching-Chun Huang

Confirmation Number:

Attorney Docket Number: NAUP0523USA

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Documents being submitted:	Files
-	
us-assignment	NAUP0523-usassn.xml
-	us-assignment.xsl
	us-assignment.dtd
	NAUP0523ASS1.tif
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us-request	NAUP0523-usrequ.xml
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us-fee-sheet	NAUP0523-usfees.xml
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us-declaration	NAUP0523-usdecl.xml
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us-power-of-attorney-grant	NAUP0523-uspoat.xml
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application-body	NAUP0523-trans.xml
	us-application-body.xsl
	application-body.dtd
	wipo.ent
	mathml2.dtd
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	isoamsa.ent
	isoamsb.ent
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	soextblx.dtd
	Naup0523usa-01.tif
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_	Naup0523usa-04.tif

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us-declaration	NAUP0523DEC1.tif	
us-declaration	NAUP0523DEC2.tif	